

Listing of Claims

1-17. (CANCEL)

18. (CURRENTLY AMENDED) An amplifier comprising:

a main amplifier subcircuit and an error amplifier subcircuit mounted together on a single circuit board;

a chassis body;

a lid structure for positioning with the chassis body to contain the circuit board and main and error amplifier subcircuits;

one of the lid structure and the chassis body having side walls extending therefrom for defining a main amplifier cavity with subcavities to contain subcircuits of the main amplifier subcircuit and an error amplifier cavity with subcavities to contain subcircuits of the error amplifier subcircuit, the side walls extending to contact the circuit board to isolate the subcircuits;

the one of the lid structure and chassis body further including a dividing wall having multiple islands extending therefrom, the multiple islands passing through multiple cut-outs formed in the circuit board between the main amplifier subcircuit and respective cavity and the error amplifier subcircuit and respective cavity to electrically couple to the other of the lid structure and chassis body to separate the amplifier cavities and electrically isolate the main and error amplifier subcircuits.

19-22. (CANCEL)

23. (CURRENTLY AMENDED) The amplifier of claim 22 wherein the ~~at least one sidewall~~ side walls are is integrally formed with the lid structure.

24. (CURRENTLY AMENDED) The amplifier of claim 22 wherein the side walls extend from the lid structure and further comprising additional subcircuits, the lid structure including at least one other ~~sidewall~~ side wall extending from a side of the lid structure opposite the ~~at least one sidewall~~ side wall for isolating subcircuits on both sides of the lid structure.

25. (CANCEL)

26. (CURRENTLY AMENDED) The amplifier of claim 22 further comprising a gasket coupled between the ~~sidewall~~ side wall and a circuit board for isolating the subcircuit.

27-32. (CANCEL)

33. (CURRENTLY AMENDED) A method of isolating subcircuits of an amplifier comprising:

mounting a main amplifier subcircuit and an error amplifier subcircuit on a single circuit board in a chassis body;

positioning a lid structure with the chassis body to contain the circuit board and amplifier subcircuits, at least one of the lid structure and the chassis body having side walls extending therefrom for defining a main amplifier cavity with subcavities to contain

subcircuits of the main amplifier subcircuit and an error amplifier cavity with subcavities to contain subcircuits of the error amplifier subcircuit;

positioning the single circuit board so that the side walls extend to contact the circuit board to isolate the subcircuits within the main amplifier cavity and the error amplifier cavity;

positioning a dividing wall, depending from one of the lid structure and the chassis body, between the main amplifier cavity and the error amplifier cavity;

passing multiple islands, extending from the a dividing wall, through multiple cut-outs formed in the circuit board between the main amplifier subcircuit and respective cavity and the error amplifier subcircuit and respective cavity to electrically couple to the other of the lid structure and chassis body to separate the amplifier cavities and electrically isolate the main and error amplifier subcircuits.

34 and 35. (CANCEL)

36.(New) The method of claim 33 wherein the side walls are integrally formed with the lid structure.

37.(New) The method of claim 33 wherein the side walls extend from the lid structure and the lid structure includes at least one other side wall extending from a side of the lid structure opposite the other side walls, and further comprising positioning additional subcircuits proximate the one other side for isolating subcircuits on both sides of the lid structure.

38.(New) The method of claim 33 further comprising coupling a gasket between the side wall and a circuit board for isolating the subcircuit.

39. (New) An amplifier comprising:

a main amplifier subcircuit and an error amplifier subcircuit mounted together on a single circuit board;

a chassis body;

a lid structure for positioning with the chassis body to contain the circuit board and main and error amplifier subcircuits;

the lid structure having side walls extending therefrom for defining a main amplifier cavity with subcavities to contain subcircuits of the main amplifier subcircuit and an error amplifier cavity with subcavities to contain subcircuits of the error amplifier subcircuit, the side walls extending to contact the circuit board to isolate the subcircuits;

the lid structure further including a dividing wall having multiple islands extending therefrom, the multiple islands passing through multiple cut-outs formed in the circuit board between the main amplifier subcircuit and respective cavity and error amplifier subcircuit and respective cavity to electrically couple to the chassis body to separate the amplifier cavities and electrically isolate the main and the error amplifier subcircuits.